

1/10

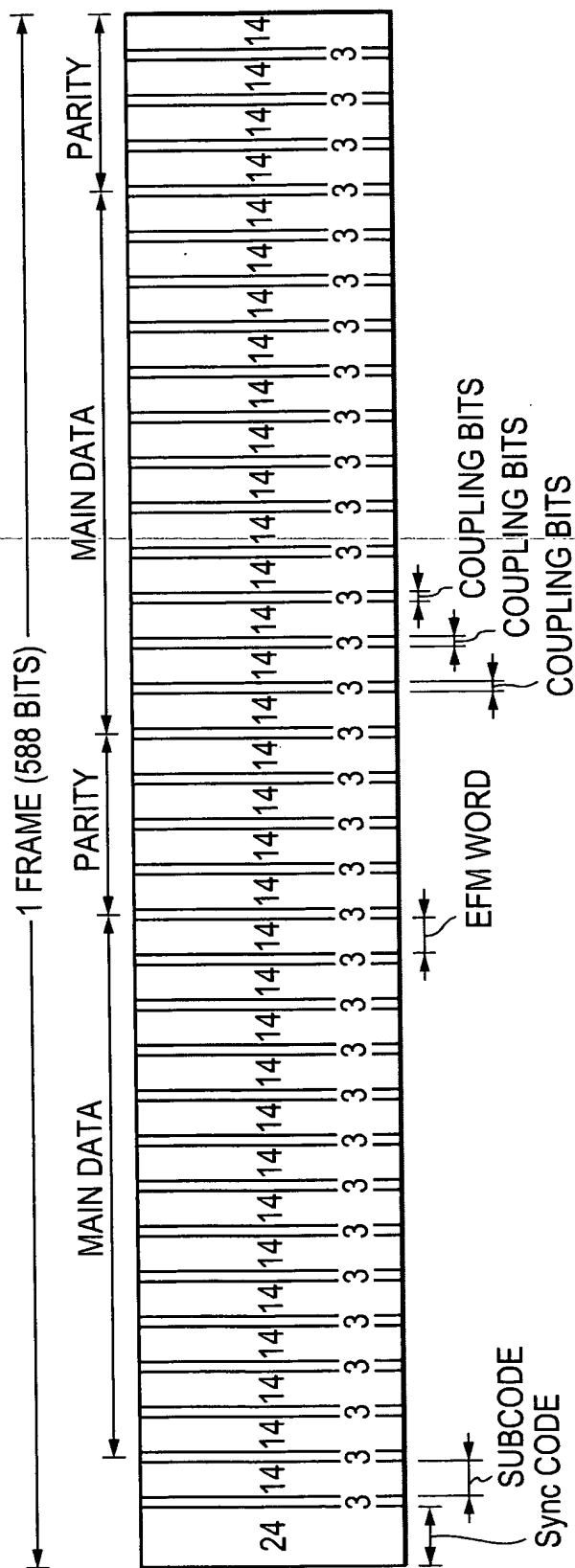
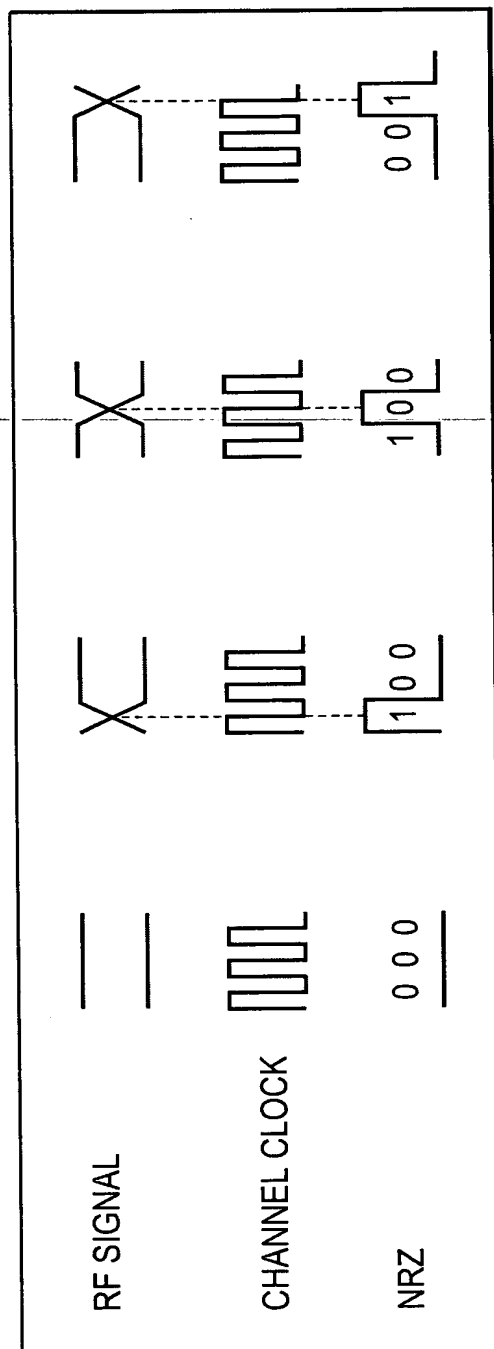


FIG.1

11T 11T 2T

FIG. 2

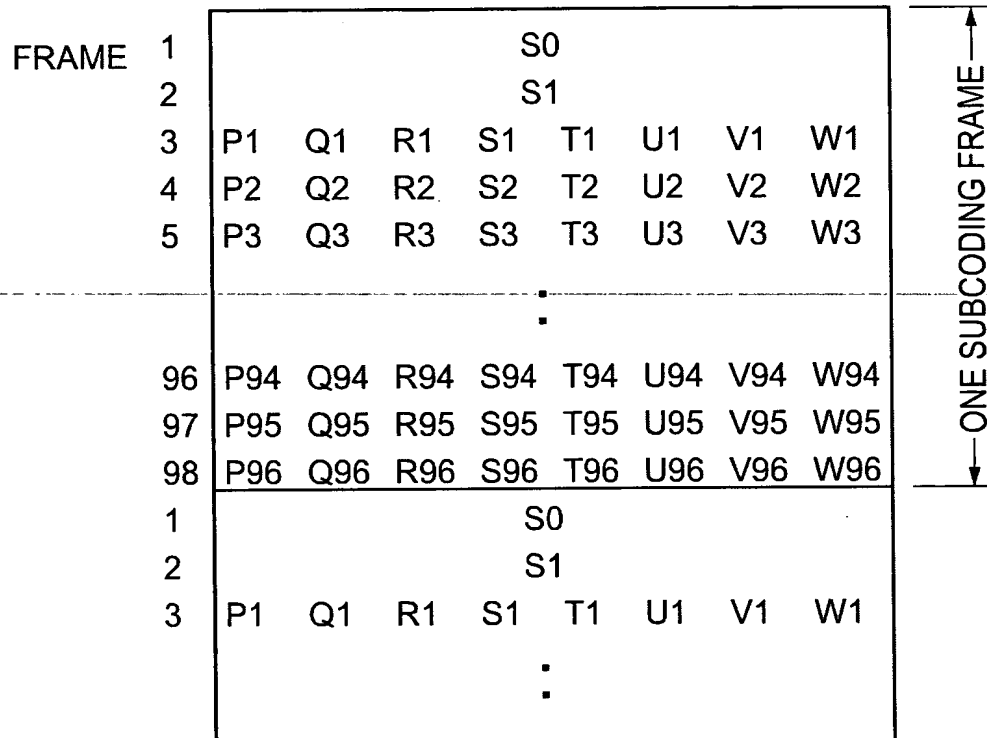
3/10



PATTERN OF COUPLING BITS

FIG.3

4/10



S0=001000000000001
S1=000000000010010

FIG.4

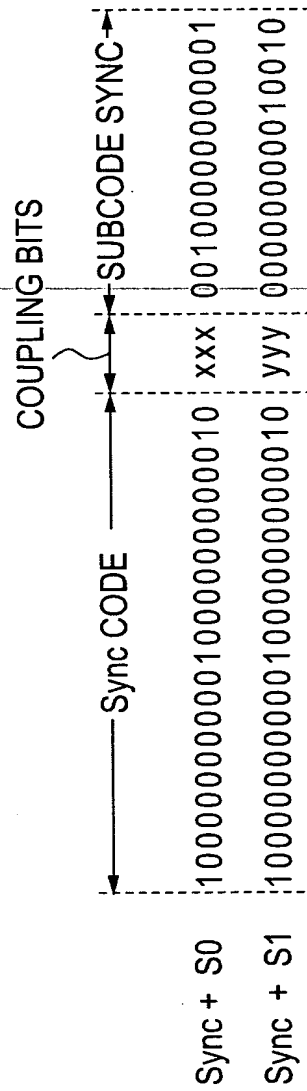


FIG.5

6/10

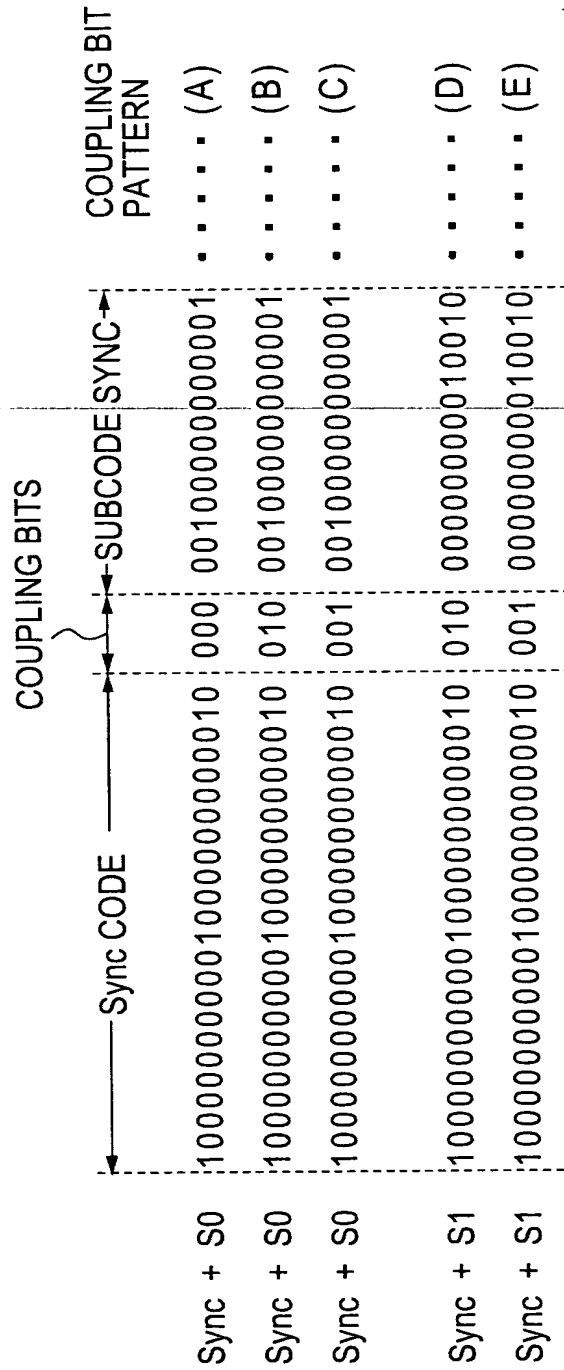


FIG.6

7/10

S0/S1	COUPLING BIT PATTERN	CONTENTS
S0[0]	A	S(Sync)
S1[0]	D,E=(0,1)	P(Parity)
S0[1]	B,C=(0,1)	K[7] (MSB)
S1[1]	D,E=(0,1)	K[6]
S0[2]	B,C=(0,1)	K[5]
S1[2]	D,E=(0,1)	K[4]
S0[3]	B,C=(0,1)	K[3]
S1[3]	D,E=(0,1)	K[2]
S0[4]	B,C=(0,1)	K[1]
S1[4]	D,E=(0,1)	K[0] (LSB)

FIG.7

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8/10

S0/S1	COUPLING BIT PATTERN	CONTENTS
S0[0]	A	S(Sync)
S1[0]	D,E=(0,1)	P(Parity)
S0[1]	B,C=(0,1)	K[7] (MSB)
S1[1]	D,E=(0,1)	K:inv[7](INVERSION BIT)
S0[2]	B,C=(0,1)	K[6]
S1[2]	D,E=(0,1)	K:inv[6]
S0[3]	B,C=(0,1)	K[5]
S1[3]	D,E=(0,1)	K:inv[5]
S0[4]	B,C=(0,1)	K[4]
S1[4]	D,E=(0,1)	K:inv[4]
S0[5]	B,C=(0,1)	K[3]
S1[5]	D,E=(0,1)	K:inv[3]
S0[6]	B,C=(0,1)	K[2]
S1[6]	D,E=(0,1)	K:inv[2]
S0[7]	B,C=(0,1)	K[1]
S1[7]	D,E=(0,1)	K:inv[1]
S0[8]	B,C=(0,1)	K[0](LSB)
S1[8]	D,E=(0,1)	K:inv[0]

FIG.8

9/10

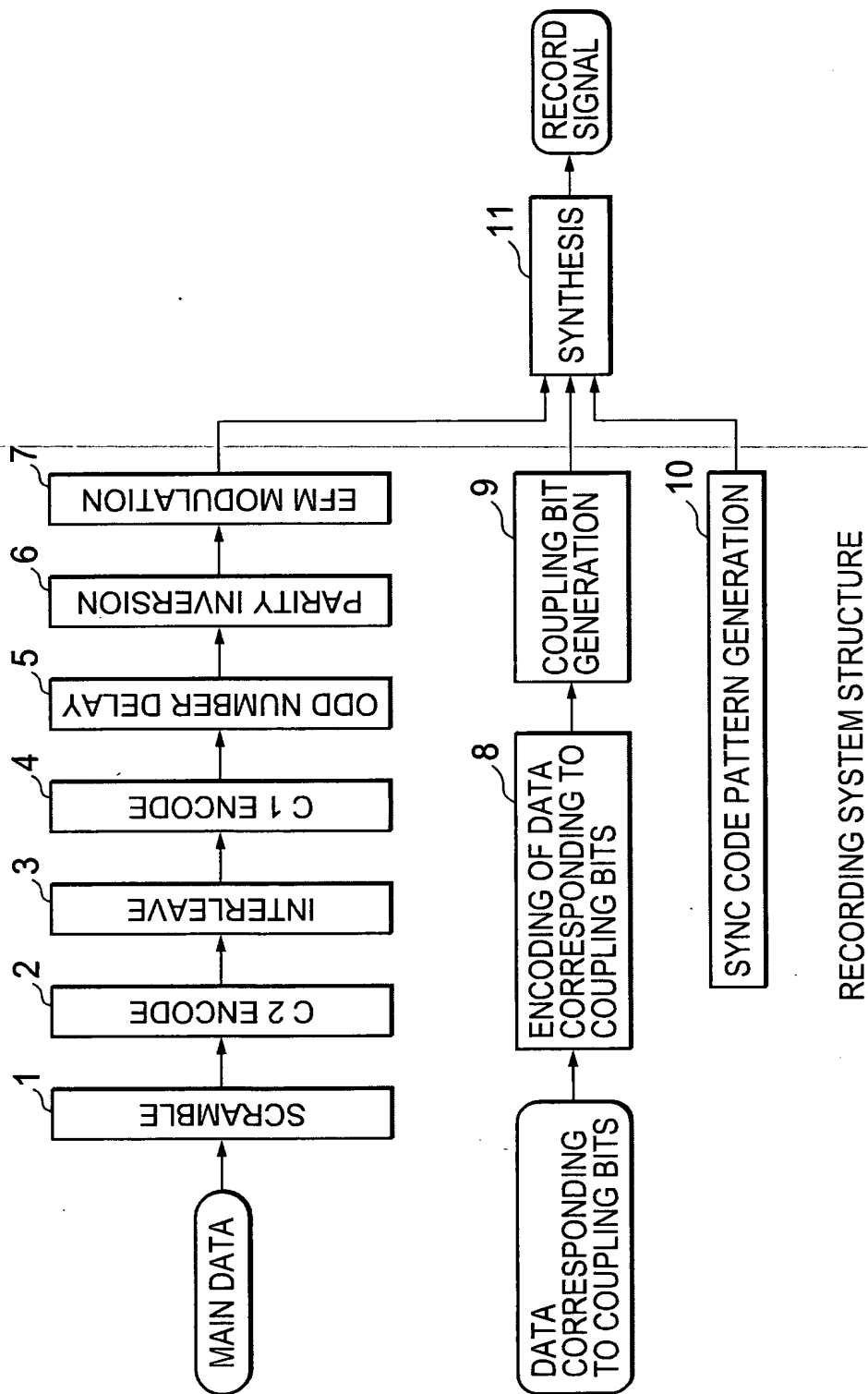


FIG.9

10/10

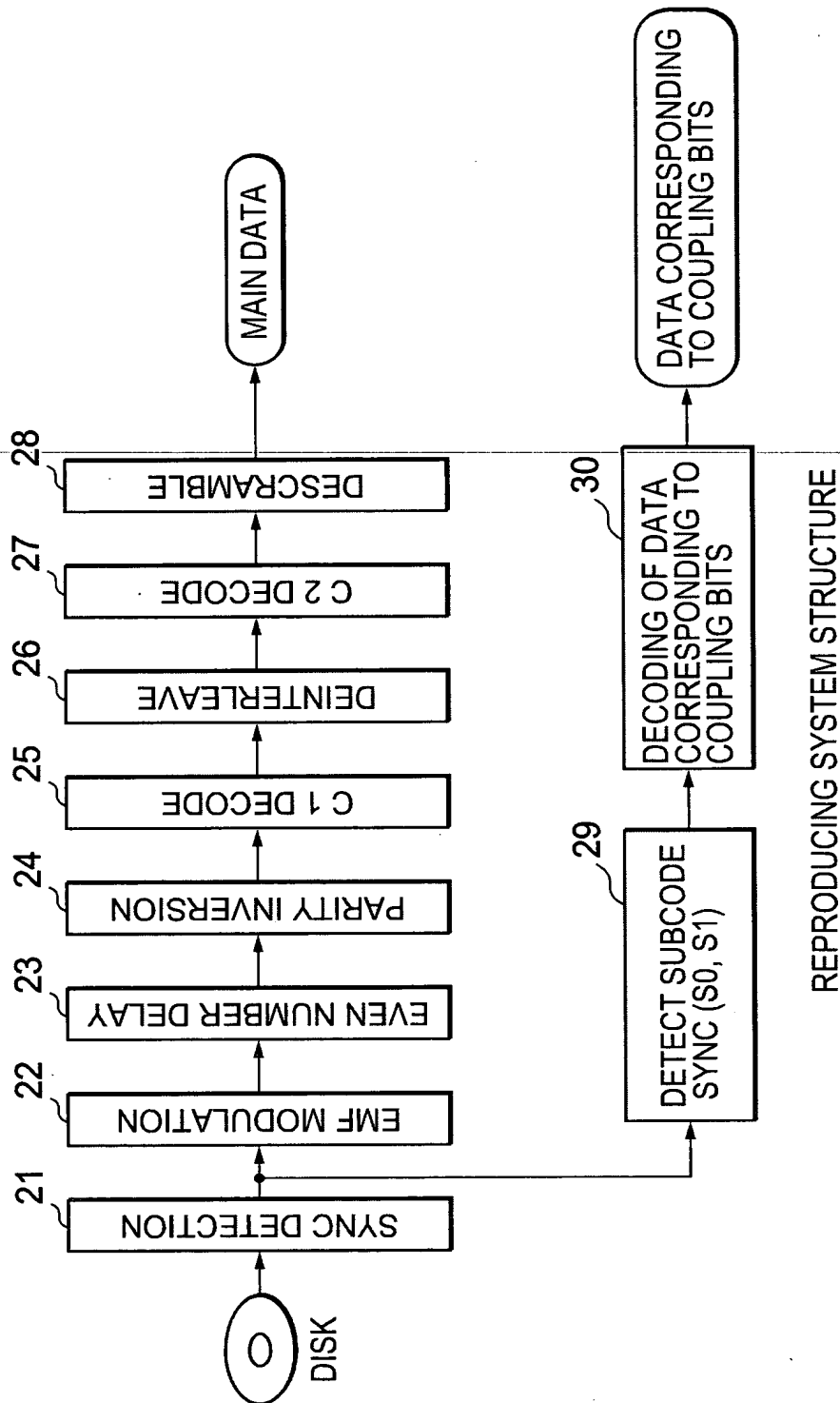


FIG.10